寶　　號
CUSTOMER　IVENT

ROHS
COMPLIANT

承　認　書
SPECIFICATION FOR APPROVAL

品名
PRODUCT NAME　FLCA-322522-100JT02

客戶料號
CUSTOMER PART NO. _____

批號
SAMPLE NO. _____

日期
DATE.　2021/9/8

Customer Approved Status 客戶承認範圍

FULLY APPROVED (全部承認)	PARTILLY APPROVED (部份承認)	REVISE APPROVED (修訂承認)
Drawn by	Checked by	Approved by
陳雯欣	吳明珠	吳炳勳

KING CORE ELECTRONICS INC.
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修訂履歷 REVISION RESUME

日期 DATE	版次 VERSION	修訂目的 PURPOSE	修訂內容概述 REVISION DESCRIPTION	備註 REMARK
2021/9/8	A	首次送樣 INITIAL RELEASE		

製作 DRAWN BY:	確認 CHECKED BY:	承認 APPROVED BY:
陳雯欣	吳明球	吳炳勳

鈞寶電子工業股份有限公司
KING CORE ELECTRONICS INC

SPECIFICATION FOR APPROVAL

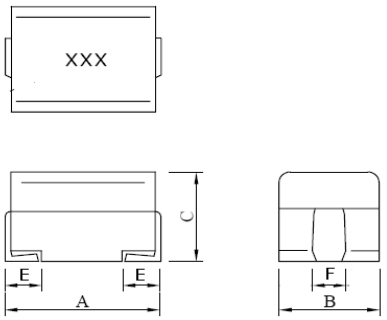
Customer: IVENT	K.C.Dwg.No. 20219001A
Customer's P/N:	K.C.P/N.

(1) ORDERING CODE

FLC A- 3 2 2 5 2 2 - 100 J T 01

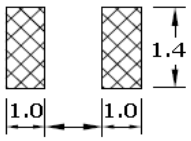
- FLC A- : Symbol of Product
- 3 2 2 5 2 2 : Dimensions
- 100 : Inductance
- J : Inductance Tolerance±5%
- T : Tape and Reel
- 01 : Ref.Code

(2)Mechanical Dim.



A:3.2±0.4 B:2.5±0.2

C2.2±0.2 E:0.6 REF F:1.0REF



(PCB Pattern)

(3) Electrical Characteristics

Type / Spec.	L(uH) 2.52 MHz	Q 2.52 MHz	SRF MHz	RDC OHM	
Specification	10±5%	30 min	30 TYP	2.1 max	
sample#1	10.17	41.8	35.9	1.81	
sample#2	10.20	40.7	36.0	1.78	
sample#3	10.20	40.7	35.8	1.80	
sample#4	10.19	41.3	35.0	1.81	
sample#5	10.26	41.3	35.6	1.80	
sample#6	10.20	40.9	36.0	1.81	
sample#7	10.21	41.8	35.4	1.82	
sample#8	10.22	41.9	35.7	1.83	
sample#9	10.16	41.5	36.1	1.82	
sample#10	10.24	41.9	35.8	1.80	
$\overline{X}$	10.21	41.4	35.7	1.81	
R	0.020	1.2	1	0.1	

(5) Condition In Testing:

..... Temp. 25^{0}C

..... Humidity: 65%

(6) Test Instruments:

...Impedance: HP 4291B RF IMPEDANCE/MATERIAL ANALYZER

...RDC: HIOKI 3540

….Isat: HP4284A+HP42841A

1…Isat Inductance drop 10%:150mA(max)

Drawn by:	Checked by:	Approved by:
陳雯欣 2021/9/8	吳明珠	吳炳勳

ER029

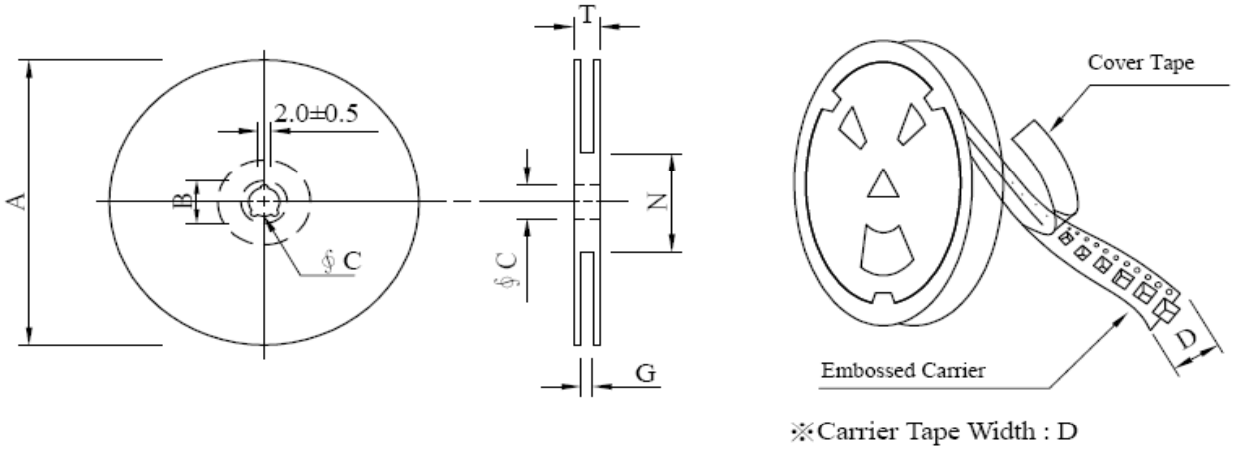
King Core Electronics Inc.

FLCA-322522 PACKAGING

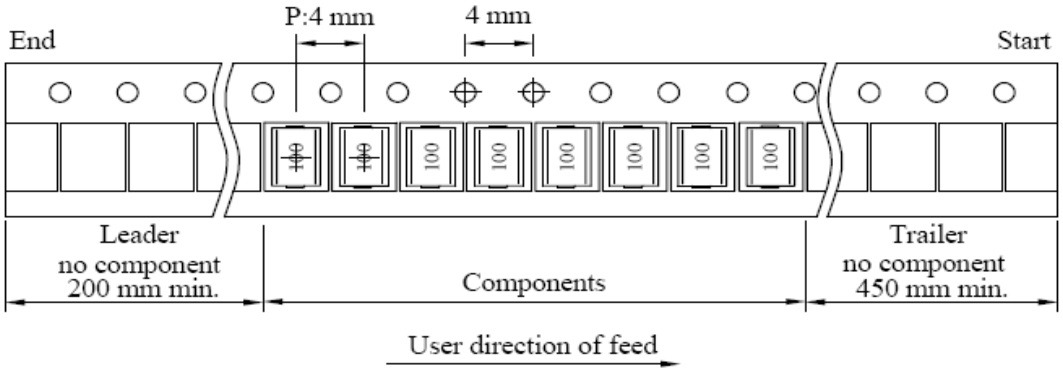
2000PCS/REEL

VI-1 . Packaging information :

(1) Configuration



※Carrier Tape Width : D



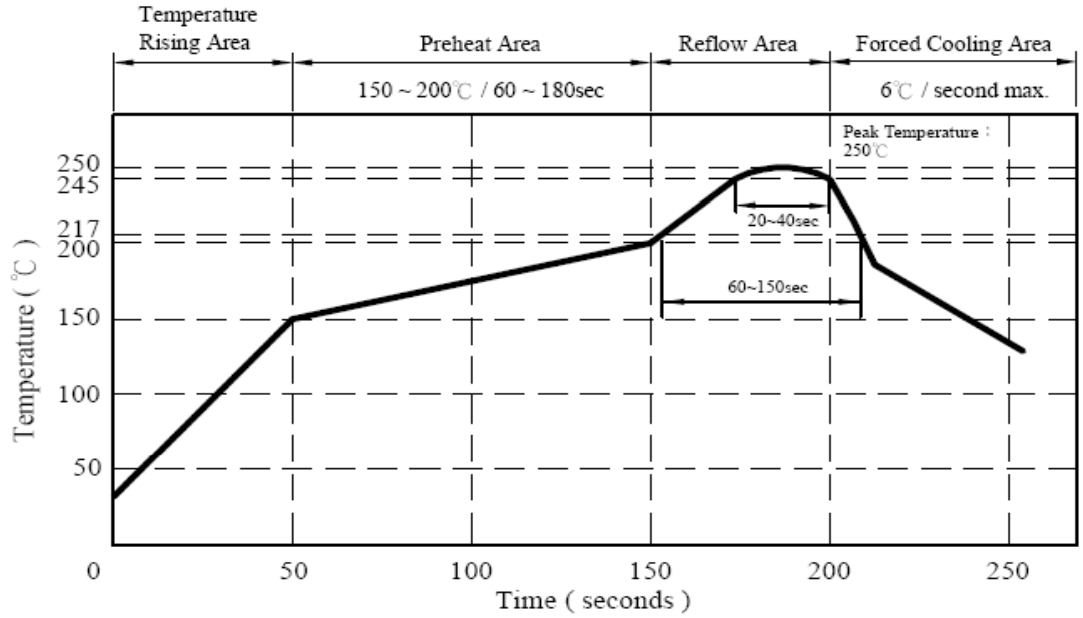
(2) Dimensions

Unit:mm

Style	A	B	C	D	G	N	T
07 - 08	178	21±0.8	13	8	10^{+0}	50^{-0}	12.5
07(S) - 08	183	21±0.8	13	8	10^{+0}	50^{-0}	12.5

General Specification：

Reflow profile
Peak Temp：250℃ max.
Max time above 245℃ ：20~40sec max.
Max time above 217℃ ：60~150sec max.
200℃~250℃ Average Ramp-up Rate：3℃/second max.



a．Temp. rise ：20℃ max.

b．Ambient temp.：100℃ max.

c．Storage temp.：-40℃ ----+125℃

d．Operating temp.：-40℃----+125℃
(Temp. rise included)

e．Terminal pull strength：1.5 kg min.

f．Rated current：Current cause
inductance drop within 10%

g．Resistance to solder heat：260℃.10 secs.

h．Resistance to solvent：Per MIL-STD-202F

Reliability test：

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2℃ 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40℃ ~ +125℃ 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature：85±2 ℃ 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
4.Operational Life	JESD22-A 108	1.Temperature: 125℃ (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperarance. 2.No marking blurred. 3.Inductance shall not change more than ±10%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature：250±5℃. 2.Time（temp.≧ 217℃）：60~150 Second. 3.IR reflow times：3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
10.Saturation Current	JIS C 6436 & User SPEC.	1.Applied rated current for 5 second. 2.Rated current	Inductance shall not drop more than 10% max.
11.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
12.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 20℃ max.
13.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing：150±5℃ / 16Hours±30 min. 2.Peak temperature：240±5℃ 3.Time（temp.≧217℃）：60~150 second. 4.IR reflow times：1 times.	More than 95% soldering coverage min on terminations.
14.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature：-40℃~125℃ 2.Room temperature：25℃.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
15.Withstanding Voltage Test	MIL-STD-202 Method 301 & User SPEC.	1.DC: 500 V (Terminal to Coating) 2.Time：1minutes	1.During the test no breakdown. 2.No mechanical or electrical damage.
16.Insulation Resistance	MIL-STD-202 Method 302	DC voltage 100V applied between inductor terminal and coating for 1 minute.	1.IR = 1000MΩ Min. 2.No mechanical or electrical damage.
17.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. pcb and dropped down from a heigh of 1m 2.Drop total time：6 time (Every side ofsample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
18.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.